

**RECEIVER CHARACTERIZATION USING PERIODIC  
SMALL-SIGNAL ANALYSIS**

## TABLE OF CONTENTS

Abstract .....	1
Introduction .....	1
Periodic small-signal analysis .....	1
Periodic AC analysis .....	1
Measuring intermodulation distortion using PAC .....	2
Periodic transfer function analysis .....	2
Periodic noise analysis .....	3
Characterizing the receiver .....	3
Conversion gain .....	4
Noise figure .....	4
Intermodulation distortion .....	4
Conclusion .....	4
References .....	4

## TABLE OF FIGURES

Figure 1	The small-signal response of a mixer as computed by PAC analysis .....	2
Figure 2	Intermodulation distortion .....	2
Figure 3	The transfer functions computed by PXF analysis .....	3
Figure 4	How noise is moved around by a mixer .....	3
Figure 5	The block diagram of the high-performance image-rejection receiver being characterized .....	3
Figure 6	The transfer functions from the $V_{rf}$ input to $V_{out}$ .....	4
Figure 7	The noise figure from $V_{rf}$ , assuming $50 \Omega$ source resistance .....	4

# Receiver Characterization using Periodic Small-Signal Analysis

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**Abstract**— Periodic small-signal analysis is a recently developed approach for AC and noise analysis of linear periodically-varying circuits. This paper presents several new approaches to characterizing the transfer functions, noise, and distortion of communication circuits using this new technique. A receiver is used as an example, but the techniques presented are applicable to a wide variety of circuits, including mixers, samplers, sample-and-holds, chopper-stabilized amplifiers, multipliers and switched-capacitor filters.

## I. INTRODUCTION

The increasing demand for low-cost mobile communication systems has greatly expanded the need for simulation algorithms that are both efficient and accurate when applied to RF communication circuits such as mixers. The difficulty is that RF circuits often process signals that contain closely-spaced large frequencies. For example, a typical cellular phone service consists of 10 kHz voiceband channels near 900 MHz. A SPICE transient analysis is extremely expensive for circuits that contain such signals because it needs to take very small time steps to follow the 900 MHz carrier, and simulate for a long time to adequately resolve the voiceband modulation. However, circuits such as amplifiers can be linearized about their DC operating point and a small-signal analysis such as AC analysis used to efficiently determine the sinusoidal steady-state response of the circuit over an arbitrarily range of frequencies. The same is not true of circuits such as mixers or switched-capacitor filters that must be driven with an additional large periodic stimulus in order to operate properly.

## II. PERIODIC SMALL-SIGNAL ANALYSIS

*Periodic small-signal analyses* address periodically-driven circuits using a two-step process. First, the *periodic operating point* is found by performing a *periodic steady-state (PSS) analysis* with only the large periodic stimulus applied. For the case of a mixer, only the LO is applied; with switched-capacitor filters, only the clock is applied.

The circuit is then linearized about the periodic operating point, and the steady-state response of the periodically-varying linear circuit is computed assuming that it is driven with a small sinusoid at an arbitrary frequency. The time required to compute the response of the linearized circuit is about the same as needed to compute the periodic steady-state response to the large stimulus alone, regardless of the frequency of the sinusoid.

An important aspect of periodically-driven circuits such as mixers and switched-capacitor filters is that they exhibit frequency translation. Conventional small signal analyses (AC and noise) linearizes about the DC operating point to create a linear time-invariant representation of the circuit, which cannot exhibit frequency conversion effects. With the periodic small-signal analyses, the circuits are linearized about a time-varying operating point and so can exhibit frequency conversion effects.

The original approach to periodic small-signal analysis used Gaussian elimination to factor and solve the set of  $N$  dense linear equations that describe the periodically-varying linearized circuit at a particular frequency where  $N$  is the number of nodes [1], [2]. The time required to factor a dense  $N \times N$  matrix is on the order of  $N^3$ , which limits this approach to circuits containing at most a few hundred nodes. However, by applying matrix-free Krylov-subspace techniques, the time required to solve the system of equations can be reduced to the order of  $N$ , allowing circuits with up to 5000 nodes to be simulated over a broad range of frequencies in a few hours [3], [4].

### A. Periodic AC Analysis

The periodic AC (PAC) analysis computes the responses of a periodically driven circuit to a small sinusoidal input at an arbitrary frequency.

When applying a small sinusoid to a linear time-invariant circuit, the steady-state response is a sinusoid at the same frequency. However, when applying a small sinusoid to a linear periodically time-varying circuit, the circuit responds with sinusoids at many frequencies, as shown in Figure 1. Thus, in a sense, PAC computes a series of transfer functions, one for each frequency. However, with these transfer functions the input and output frequencies are offset by the harmonics of the LO.

The small signal frequency can be arbitrarily close, or

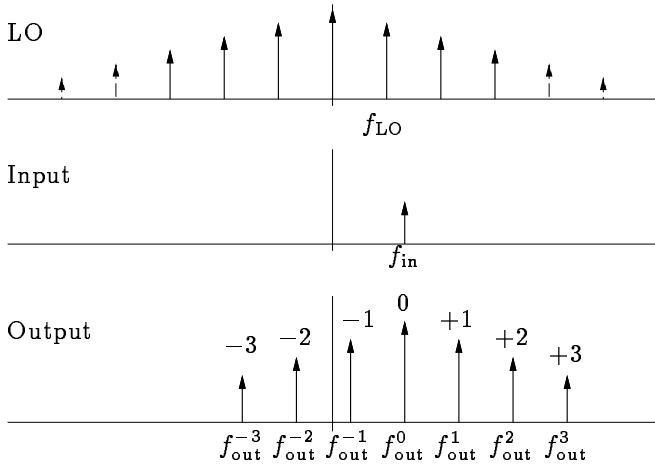


Fig. 1. The small-signal response of a mixer as computed by PAC analysis. The input signal is replicated and translated by each harmonic of the LO. In down-conversion mixers, the  $-1$  sideband usually represents the IF output.

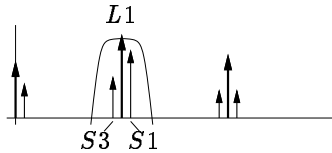


Fig. 2. Intermodulation distortion is efficiently measured by applying one large tone ( $L1$ ), performing a PSS analysis, and then applying the second small tone ( $S1$ ) with a PAC analysis. In this figure,  $V_{L1}$  is the fundamental of the response due to the large input tone,  $V_{S1}$  is the fundamental of the response due to the small input tone and is the upper sideband of  $V_{L1}$ .  $V_{S3}$  is the lower sideband of  $V_{L1}$ , and represents the intermodulation distortion.

even equal to, the LO frequency with no performance penalty.

### B. Measuring Intermodulation Distortion using PAC

A important use of PAC analysis is to measure the intermodulation distortion of amplifiers and mixers. The traditional approach to measuring intermodulation distortion is to apply two large closely spaced tones to the input and measure the third-order intermodulation products. Applying this approach with a circuit simulator is expensive because two tones that are closely spaced in frequency require a long analysis interval. The approach used with PSS/PAC is slightly different. Here only one large tone is applied. Then PAC is used to apply the second tone close to the first. If the small input signal is considered to be one sideband of the large input signal, then the response at the other sideband is the third-order intermodulation distortion, as shown in Figure 2. In this case, the first tone (the tone applied in PSS analysis) drives the circuit hard enough to cause distortion and the second tone (the one applied in PAC) is a test tone that is only used to measure the intermodulation distortion. Once  $V_{L1}$ ,  $V_{S1}$ , and  $V_{S3}$  are

measured in decibels, the third-order intercept point is

$$IP_3 = V_{L1} - \frac{V_{S3} - V_{S1}}{2}. \quad (1)$$

The intermodulation distortion of a mixer is measured in a similar manner, except the PSS analysis must include both the LO and the one large tone.

This approach gives slightly different results than the approach typically used when measuring intermodulation distortion on the bench. There, two large tones of equal amplitude are applied, both of which drive the circuit to behave nonlinearly. In this new method, only one tone is large enough to drive the circuit to behave nonlinearly. To mimic the PSS/PAC approach on the bench, simply reduce the amplitude of one of the tones. Both approaches are measuring intermodulation distortion; they simply use different ways of quantifying the results. Typically, when distortion levels are small, which they must be when computing  $IP_3$  using (1), then the two methods produce results that are within one or two decibels.

Besides dramatically reducing the simulation time and memory, the PSS/PAC approach has other interesting benefits. If intermodulation distortion is measured using two large tones, then two intermodulation tones are produced and they are usually slightly different. A difference in amplitude in the intermodulation products usually indicates that the amplitude response of the circuit as a function of frequency is not flat at the measurement frequency. (It might also indicate that the two tones are not precisely the same size). On the bench, one can make the two test tones closer in frequency to reduce this effect, but this approach dramatically increases the time required for simulation. With the PSS/PAC approach, the separation between the tones can be arbitrarily small or they can be even at the same frequency. In this case, the ambiguity is resolved by accessing the signals by sideband index, rather than frequency, enabling accurate measurement of intermodulation distortion even where the frequency response is not flat, such as along the skirt of a filter.

### C. Periodic Transfer Function Analysis

A periodic transfer function (PXF) analysis computes the transfer function from every source in the circuit at any frequency to a single output at a single frequency. It differs from a PAC analysis in that the PAC analysis computes the response from a single stimulus at a single frequency to every node in the circuit at every frequency. This is illustrated in Figure 3. Generally, PAC is used when several transfer functions are needed that share the same input, and PXF is used when several transfer functions are needed that share the same output. The PXF analysis can simultaneously compute such useful quantities as conversion efficiency (transfer function from input to output at desired frequency), image and sideband rejection (input to output at undesired frequency), and LO feed-through and

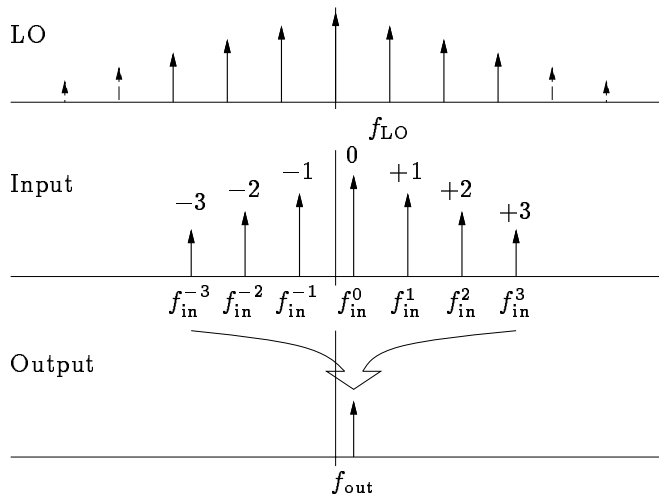


Fig. 3. The transfer functions computed by PXF analysis. The output is sensitive to signals at many frequencies at the input of the mixer. The input signals are replicated and translated by each harmonic of the LO. The ones shown are the ones that end up at the output frequency of interest.

power supply rejection (undesired input to output at all frequencies). Frequency translation is taken into account in these quantities, so it is possible to determine how sensitive the output is to noise either up or down-converted from the supplies or the LO.

#### D. Periodic Noise Analysis

The periodic noise (PNoise) analysis is similar to the conventional noise analysis, except that it includes frequency conversion effects. Hence is it useful for computing the noise behavior of mixers, switched-capacitor filters, chopper-stabilized amplifiers, and the like.

PNoise is a two step process where PSS is used to compute the response to a large periodic signal such as a clock or LO. Results are labeled LO and shown in Figure 4. In the second step, which is the actual PNoise analysis, the resulting noise performance is computed. With periodic systems, there are two effects that act to translate noise in frequency. First, for noise sources that are bias dependent, such as shot noise sources, the time-varying operating point acts to modulate the noise sources. Second, the transfer function from the noise source to the output is also periodically time-varying and so acts to modulate the contribution of the noise source to the output. Modulation is a multiplicative effect in the time-domain and so in the frequency-domain the spectrum of the noise source is convolved with the spectrum of the transfer function. The transfer function is periodic and so has a discrete line spectrum. Convolution with a discrete spectrum involves a countable number of scale, translate, and sum operations, in a process known as *noise folding*, as shown in the rest of Figure 4. The final result of the analysis is the sum of the noise contributions both up-converted (negative labels)

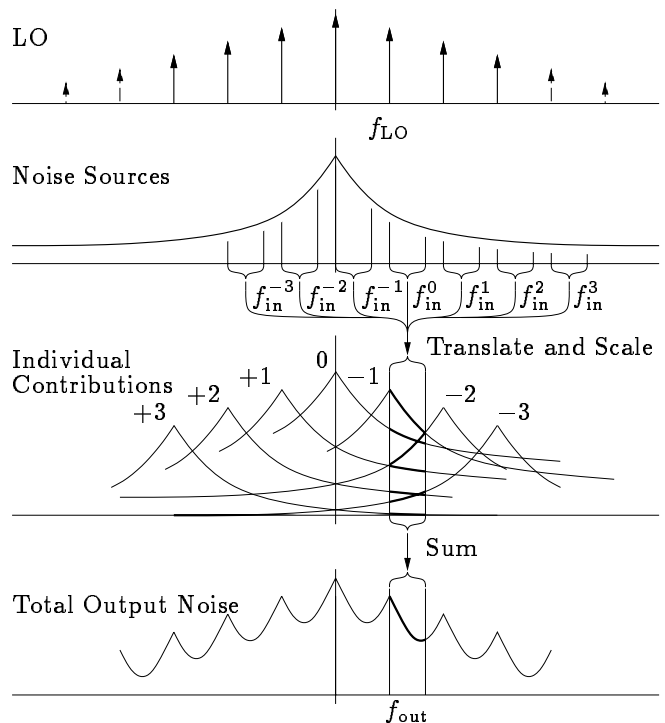


Fig. 4. How noise is moved around by a mixer. The noise is replicated and translated by each harmonic of the LO. The PNoise analysis computes the total noise including the modulation effects caused by the LO.

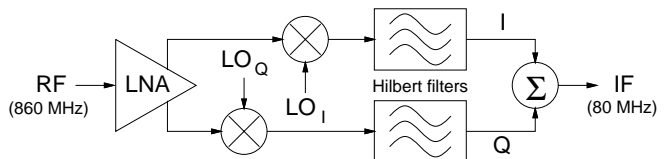


Fig. 5. The block diagram of the high-performance image-rejection receiver being characterized.

and down-converted (positive labels) to the desired output frequency.

### III. CHARACTERIZING THE RECEIVER

The receiver being characterized is a rather large RF circuit designed by Tomas Melander of Ericsson Radio, and is shown in Figure 5. It is an image rejection receiver that consists of a low-noise amplifier, a splitting network, two double-balanced mixers, and broad-band Hilbert transform output filter combined with a summing network that is used to suppress the undesired sideband. It contains 167 bipolar transistors and uses 378 nodes. It is being simulated at the circuit level using the Gummel-Poon model for each transistor. It generated 986 equations (due to the large number of inductors and the BJT's parasitic resistors).

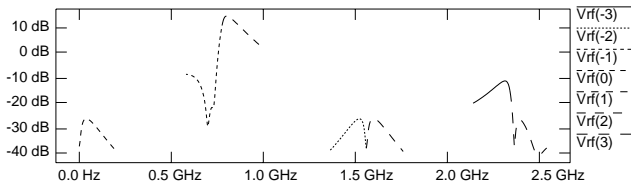


Fig. 6. The transfer functions from the  $V_{rf}$  input to  $V_{out}$ .

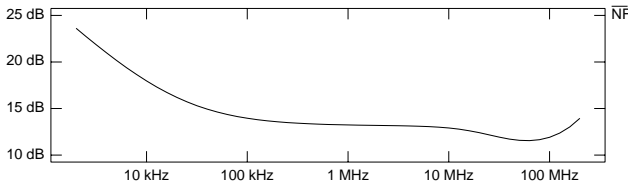


Fig. 7. The noise figure from  $V_{rf}$ , assuming  $50 \Omega$  source resistance.

### A. Conversion Gain

PXF analysis is performed to compute the conversion gain and the results are shown in Figure 6. Several transfer functions are shown. All are transfer functions from the RF input to the IF output, however each represents a different sideband. In Figure 6 we chose to label the X-axis with the input frequency. The output frequency range for all of the transfer functions shown is baseband, which is being swept from 20 kHz to 200 MHz. For example,  $V_{RF}(1)$  is the transfer function from the first upper sideband (780.02 MHz–980 MHz) to baseband (20 kHz–200 MHz), and  $V_{RF}(-1)$  is the transfer function from the first lower sideband (580 MHz–797.98 MHz) to baseband.

Less than 4 minutes were required on an HP735/125 for this analysis, which contained 50 frequency points.

### B. Noise Figure

The PNoise analysis is similar to the traditional noise analysis, except it includes the effect of noise moving from one frequency to another as it mixes with the LO and its harmonics. The noise measured at the output includes contributions from noise generated at frequencies  $f_{out} \pm k f_{LO}$ , where  $|k| \leq 7$ , and 7 is a user settable parameter.

The noise figure as computed by PNoise is shown in Figure 7. Less than 4 minutes were required on an HP735/125 for 50 frequency points.

### C. Intermodulation Distortion

The first step in computing intermodulation distortion is to configure PSS analysis to measure harmonic distortion. Thus, the LO and one large input tone are applied. Since the resulting signals must be periodic, the applied signals must be commensurate and have the smallest possible common period. In this circuit the LO is given as 780 MHz and the input tone is chosen to be 840 MHz, which gives a

16.67 ns common period. The PSS analysis takes 2.5 minutes and 93 MB to compute 787 time-points. So far, only enough information is available to compute the receiver's harmonic distortion. As a second step, a small 850 MHz signal is applied and PAC analysis is run. Since only one frequency point is needed from the PAC analysis, it is very efficient, requiring only 47 seconds to compute the perturbation to the PSS solution due to the small 850 MHz RF test signal.

$IP_3$  is computed using  $V_{L1}$ ,  $V_{S1}$ , and  $V_{S3}$ , where  $V_{L1} = -25.7$  dBV is the magnitude at the output of first fundamental signal at 60 MHz,  $V_{S1} = 2.6$  dBV is the magnitude at the output of the second fundamental signal at 70 MHz, and  $V_{S3} = -49.7$  dBV is the  $IM_3$  term at the output at 50 MHz. The third-order intercept point computed using (1) is 0.45 dBV.

To confirm this result, a 3-tone PSS analysis was performed. The results were within 1 dB, with the PSS/PAC approach requiring 6 times less time and memory. This ratio would be much greater if the receiver were followed by a narrow-band filter, and so required closer spacing of the intermod tones.

## IV. CONCLUSION

Conventional small-signal analyses, such as AC and noise analyses, are widely considered essential by those engineers involved in designing amplifiers and filters. Periodic small-signal analyses provide these same measurement capabilities for periodically-driven circuits such as mixers, detectors, samplers, frequency multipliers, phase-locked loops, oscillators, parametric oscillators, switched-capacitor and switched-current filters, chopper-stabilized and parametric amplifiers, sample-and-holds, and etc. In addition, it is shown how to use a periodic small-signal analysis to efficiently measure intermodulation distortion. It is felt that these measurement capabilities will soon be considered as essential for periodically-driven circuits as AC and noise analysis are for amplifiers and filters.

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